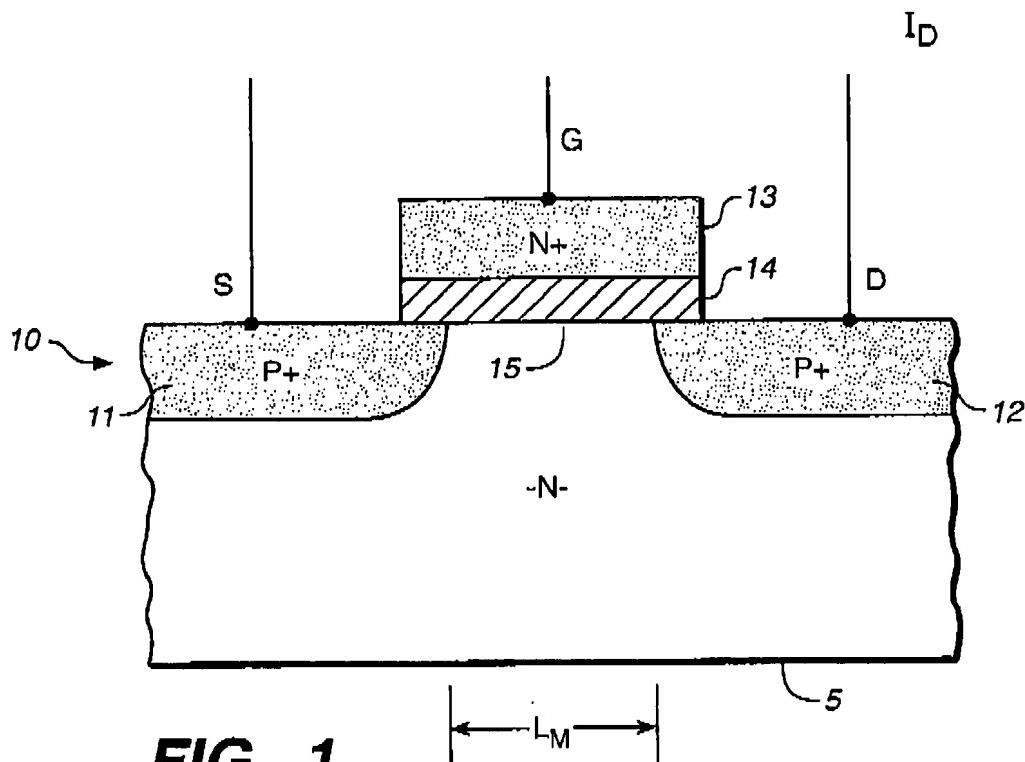
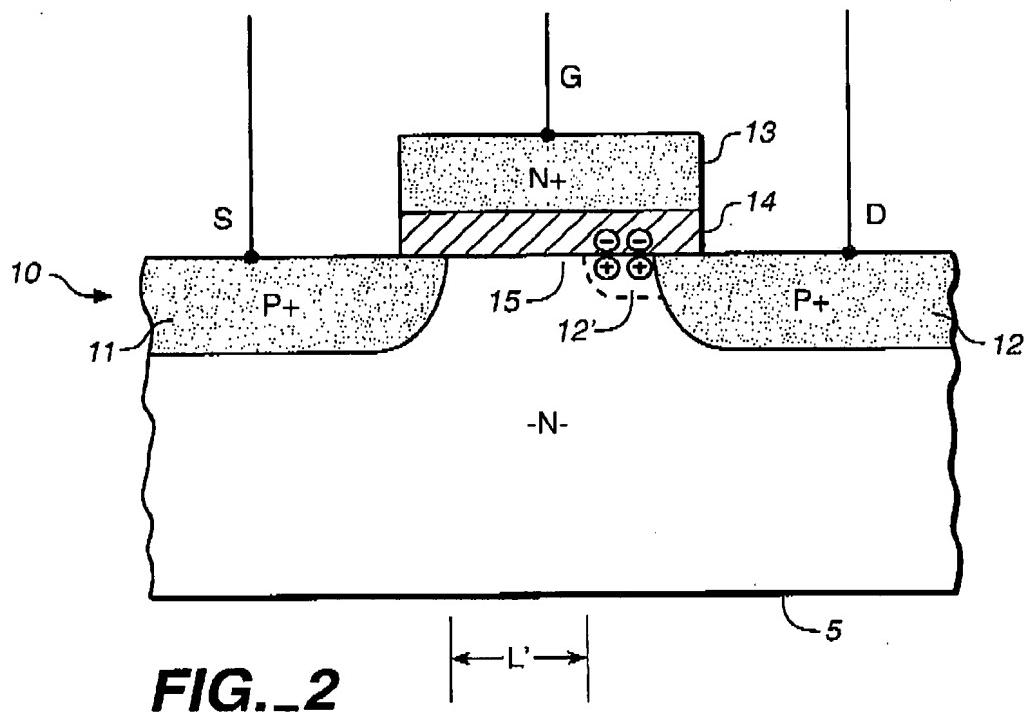


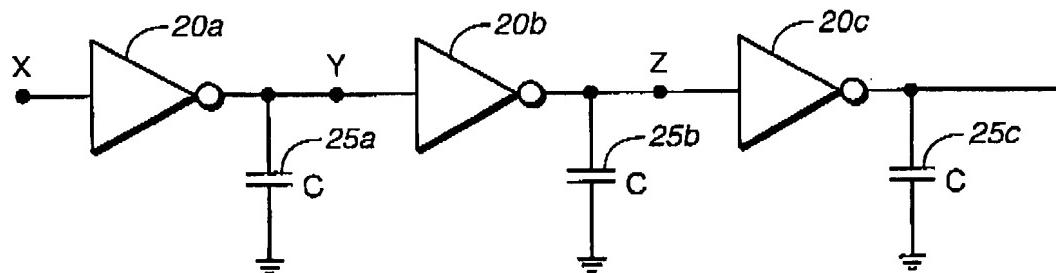
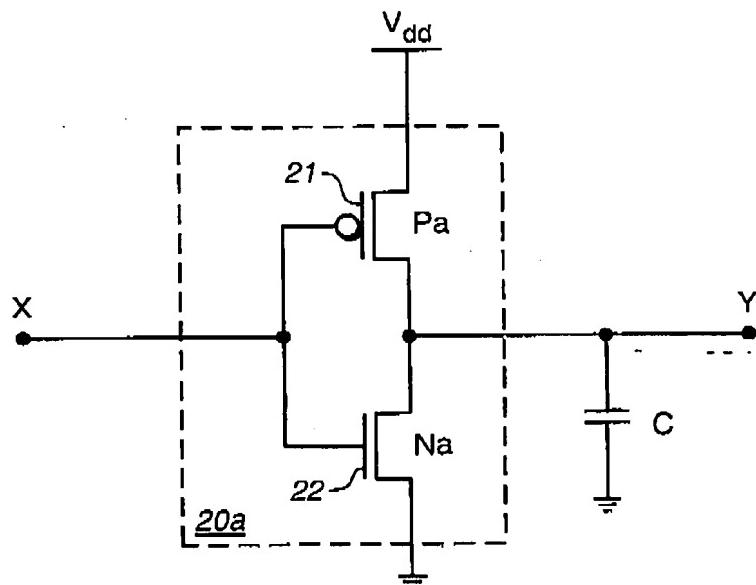
Appl. No.: 09/832,933 Docket No.: HITA1.002US1
 Title: Hot Carrier Circuit Reliability Simulation
 Inventors: Wu et al.
 Filing Date: 4/11/01 REPLACEMENT SHEET 1 of 4

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**FIG. 1****FIG. 2**

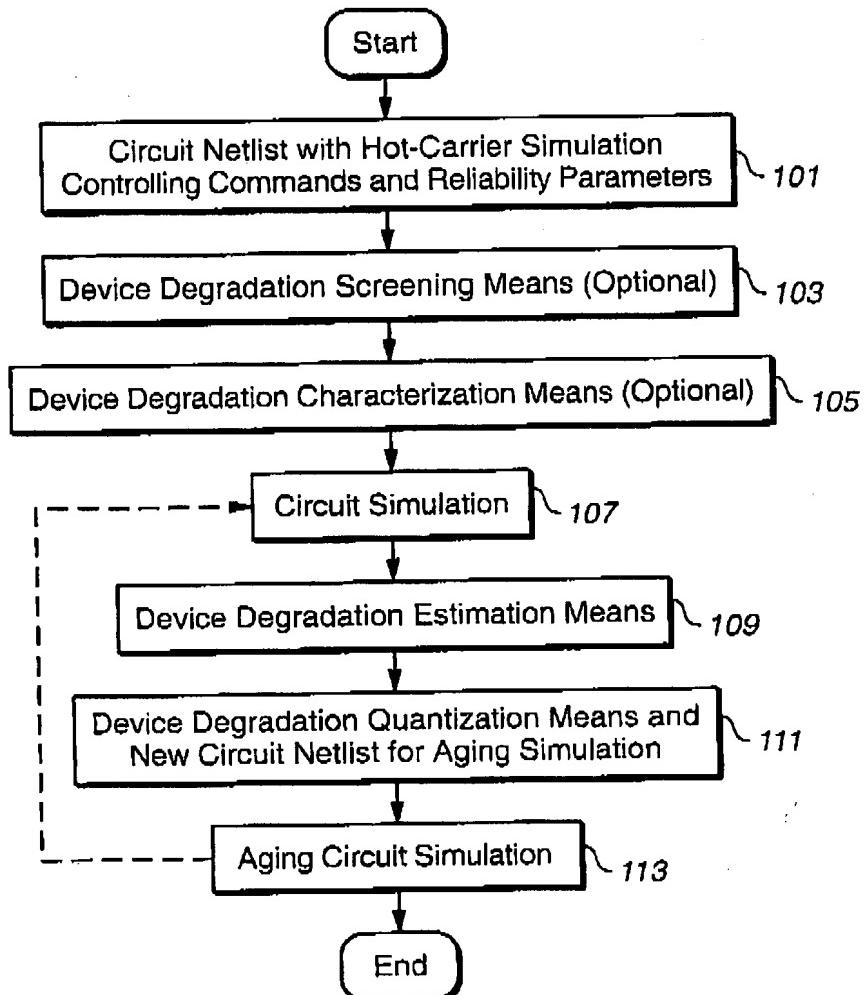
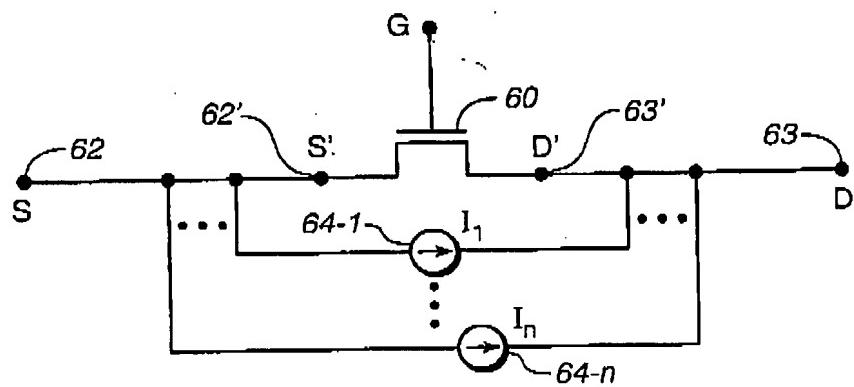
Appln. No.: 09/832,933 Docket No.: BTAT.002US1
 Title: Hot Carrier Circuit Reliability Simulation
 Inventors: Wu et al.
 Filing Date: 4/11/01 REPLACEMENT SHEET 1 of 4

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**FIG._3****FIG._4**

Appl. No.: 09/832,933 Docket No.: 8STAT-002US1
 Title: Hot Carrier Circuit Reliability Simulation
 Inventors: Wu et al.
 Filing Date: 4/11/01 REPLACEMENT SHEET 3 of 4

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**FIG._5****FIG._6**

Appn. No.: 09/833,933 Docket No.: BTAT.002US1
 Title: Hot Carrier Circuit Reliability Simulation
 Inventor: Wu et al.
 Filing Date: 4/11/01 REPLACEMENT SHEET 4 of 4

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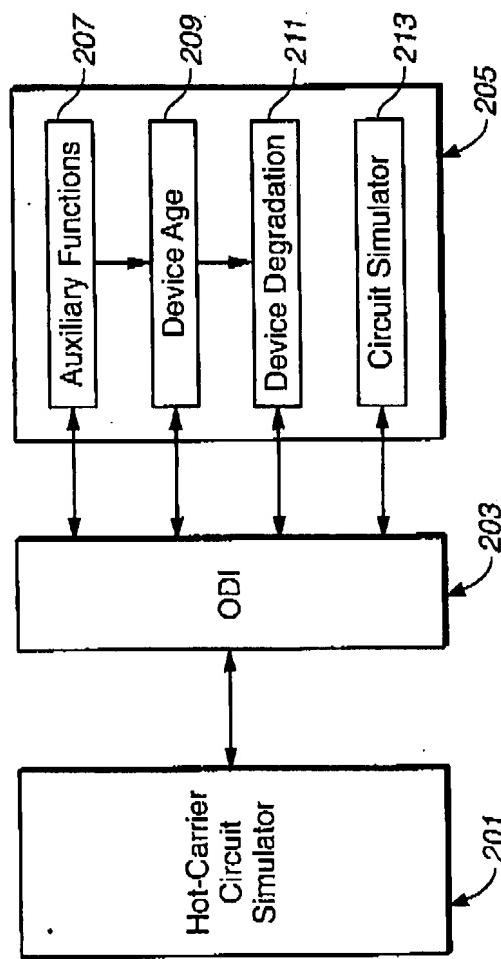


FIG.-7

```

.m1 1 2 3 4 nmos W=10u L=0.25u
.subckt NM n1 n2 n3 n4 W(VALUE) L(VALUE) age
  .model nmos nmos vth0=0.7+5*age1 ...
  m1 n1 n2 n3 n4 nmos W=W_VALUE L=L_VALUE
.ends

Xm1 1 2 3 4 10e-6 0.25e-6 age1 NM

.msubckt NM n1 n2 n3 n4 W(VALUE) AGE(VALUE)
.param age = AGE_VALUE
m1 n1 n2 n3 n4 nmos W=W_VALUE L=L_VALUE
.ends

Xm1 1 2 3 4 10e-6 0.25e-6 age1 NM
  
```

FIG.-8